	Application No.	Applicant(s)	
Notice of Allowability			
	10/007,145 Examiner	WU, MARK Art Unit	
	Examine	Art office	
	Brian L. Albertalli	2626	
The MAILING DATE of this communication appeared All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in or other appropriate commu IGHTS. This application is s	this application. If not included nication will be mailed in due course.	THIS initiative
1. X This communication is responsive to <u>01 February 2006</u> .			
2. The allowed claim(s) is/are <u>1-14,16-18 and 20-52</u> .			
3. ☐ Acknowledgment is made of a claim for foreign priority una) ☐ All b) ☐ Some* c) ☐ None of the: 1. ☐ Certified copies of the priority documents have	e been received.		
2. Certified copies of the priority documents have	· ·		
3. Copies of the certified copies of the priority do	cuments have been received	in this national stage application from	1 the
International Bureau (PCT Rule 17.2(a)).			
* Certified copies not received:			
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONM THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		a reply complying with the requiremer	nts
4. A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give			OF
5. CORRECTED DRAWINGS (as "replacement sheets") mus	st be submitted.		
(a) \square including changes required by the Notice of Draftspers	son's Patent Drawing Review	(PTO-948) attached	
1) 🗌 hereto or 2) 🔲 to Paper No./Mail Date			
(b) including changes required by the attached Examiner's Paper No./Mail Date	s Amendment / Comment or	in the Office action of	
Identifying indicia such as the application number (see 37 CFR 1, each sheet. Replacement sheet(s) should be labeled as such in t			f
6. DEPOSIT OF and/or INFORMATION about the deposit attached Examiner's comment regarding REQUIREMENT.	sit of BIOLOGICAL MATE FOR THE DEPOSIT OF BIO	RIAL must be submitted. Note the LOGICAL MATERIAL.	
Attachment(s) 1. ☑ Notice of References Cited (PTO-892)	5. □ Notice of Int	ormal Patent Application (PTO-152)	
Notice of Neterences Cited (P10-092) Notice of Draftperson's Patent Drawing Review (PTO-948)	6. ☐ Interview Su	ımmary (PTO-413),	
3. Information Disclosure Statements (PTO-1449 or PTO/SB/0	Paper No./ 98), 7. ☐ Examiner's	Mail Date Amendment/Comment	
Paper No./Mail Date 4. Examiner's Comment Regarding Requirement for Deposit	8. 🛭 Examiner's	Statement of Reasons for Allowance	
of Biological Material	9.	÷	

DETAILED ACTION

Response to Amendment

1. The amendments to the claims have been entered. Claims 1, 11-14, 16-18, and 28, are currently amended and claims 15 and 19 are currently canceled.

Response to Arguments

2. Applicant's arguments, see pages 12-17, filed February 1, 2006, with respect to claims 1-14, 16-18, and 20-52 have been fully considered and are persuasive. The rejections of claims 1-14, 16-18, and 20-52 have been withdrawn.

Claim Rejections - 35 USC § 112

3. The amendments to claims 11-14, 16, and 17 overcome the previous rejections under 35 U.S.C. 112, 2nd paragraph. Specifically, claims 11-14, 16, and 17 now clearly distinguish between "the boot program" and "the updates for the boot program".

Allowable Subject Matter

- 4. Claims 1-14, 16-18, and 20-52 are allowed.
- 5. The following is an examiner's statement of reasons for allowance:

With regard to claims 11-14, 16, and 17, while Schurect et al. teach a method of searching for updates to a boot program where a vector table is utilized, Schurect et al. do not teach *loading updates for the boot program through a controller-to-DSP gateway to a DSP program memory*, as required by independent claim 11. Rather, Schurect et

al. disclose a method wherein updates to a boot program (patch programs) are loaded directly from a program RAM. Thus, there would be no suggestion to one of ordinary skill in the art to modify Schurect et al. to include a controller-to-DSP gateway to load the programs to a DSP memory.

Claims 12-14, 16 and 17 further limit claim 11 and thus are also allowable.

Similarly, with regard to claims 1-10, 18, and 20-52, the combination of Du et al. and Altare et al. does not teach the use of a controller-to-DSP gateway through which processed sounds are passed, as required by independent claims 1, 18, and 28.

Neither Du nor Altare et al. would suggest to one of ordinary skill in the art the step/means for passing sounds to be recorded through a controller-to-DSP gateway.

Claims 2-10, 20-27, and 29-52 further limit independent claims 1, 18, and 28, and thus are also allowable.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

6. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Biscondi (*Impementing the TMS320C6201/C6701/C65211 HPI Boot Process*) describes a boot procedure for a typical DSP.

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7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Brian L. Albertalli whose telephone number is (571) 272-7616. The examiner can normally be reached on Mon - Fri, 8:00 AM - 5:30 PM, every second Fri off.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David Hudspeth can be reached on (571) 272-7843. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

BLA 4/14/06

DAVID HUDSPETH SUPERVISORY PATENT EXAMINER TECHNOLOGY CENTER 2600